

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	34975	retention adj time/	US-PGPUB; USPAT	OR	ON	2006/05/19 11:53
L2	38704	refresh	US-PGPUB; USPAT	OR	ON	2006/05/19 11:53
L3	469	1 same 2	US-PGPUB; USPAT	OR	ON	2006/05/19 11:53
L4	50233	dynamic adj random adj access adj memory	US-PGPUB; USPAT	OR	ON	2006/05/19 11:53
L5	89255	dram	US-PGPUB; USPAT	OR	ON	2006/05/19 11:53
L6	95258	4 or 5	US-PGPUB; USPAT	OR	ON	2006/05/19 11:53
L7	440	3 and 6	US-PGPUB; USPAT	OR	ON	2006/05/19 11:54
L8	22	harrand-michel.in.	US-PGPUB; USPAT	OR	ON	2006/05/19 11:54
L9	4	bulone-joseph.in.	US-PGPUB; USPAT	OR	ON	2006/05/19 11:54

Day : Friday  
 Date: 5/19/2006  
 Time: 11:59:00


**PALM INTRANET**
**Inventor Name Search Result**

Your Search was:

Last Name = HARRAND

First Name = MICHEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#"><u>07557718</u></a>	<a href="#"><u>5126734</u></a>	150	07/26/1990	SWITCHING MATRIX CROSSPOINT	HARRAND, MICHEL
<a href="#"><u>07926815</u></a>	<a href="#"><u>5265041</u></a>	150	08/07/1992	BIDIMENSIONAL FIR FILTER	HARRAND, MICHEL
<a href="#"><u>07931827</u></a>	Not Issued	166	08/18/1992	IMAGE PREDICTOR	HARRAND, MICHEL
<a href="#"><u>08111624</u></a>	<a href="#"><u>5349547</u></a>	150	08/25/1993	BIDIMENSIONAL FIR FILTER	HARRAND, MICHEL
<a href="#"><u>08240144</u></a>	<a href="#"><u>5592428</u></a>	150	05/10/1994	DYNAMIC MEMORY	HARRAND, MICHEL
<a href="#"><u>08269326</u></a>	<a href="#"><u>RE35483</u></a>	150	06/30/1994	SWITCHING MATRIX CROSSPOINT	HARRAND, MICHEL
<a href="#"><u>08385074</u></a>	<a href="#"><u>6236683</u></a>	150	02/07/1995	IMAGE PREDICTOR	HARRAND, MICHEL
<a href="#"><u>08465810</u></a>	<a href="#"><u>5590070</u></a>	150	06/06/1995	DYNAMIC MEMORY	HARRAND, MICHEL
<a href="#"><u>08525468</u></a>	<a href="#"><u>5995513</u></a>	150	09/08/1995	MULTITASK PROCESSING SYSTEM	HARRAND, MICHEL
<a href="#"><u>08990925</u></a>	<a href="#"><u>6144321</u></a>	150	12/15/1997	MICROPROCESSOR DEDICATED TO PROCESSING OF BIT STREAMS IN A SYSTEM OF COMPRESSION/DECOMPRESSION OF ANIMATED IMAGES	HARRAND, MICHEL
<a href="#"><u>08991022</u></a>	<a href="#"><u>6133859</u></a>	150	12/15/1997	MICROPROCESSOR DEDICATED TO PROCESSING OF BIT STREAMS IN A SYSTEM OF COMPRESSION/DECOMPRESSION OF ANIMATED IMAGES	HARRAND, MICHEL
<a href="#"><u>08994953</u></a>	<a href="#"><u>5987488</u></a>	150	12/19/1997	MATRIX PROCESSOR	HARRAND, MICHEL
<a href="#"><u>09047142</u></a>	Not	161	03/24/1998	CODER/DECODER OF ANIMATED	HARRAND,

	Issued			IMAGES WITH A CACHE MEMORY	MICHEL
<a href="#"><u>09276603</u></a>	<a href="#"><u>6215706</u></a>	150	03/25/1999	FAST STRUCTURE DRAM	HARRAND, MICHEL
<a href="#"><u>09420129</u></a>	<a href="#"><u>6914908</u></a>	150	10/19/1999	MULTITASK PROCESSING SYSTEM	HARRAND, MICHEL
<a href="#"><u>09715904</u></a>	<a href="#"><u>6675256</u></a>	150	11/17/2000	FAST DRAM CONTROL METHOD AND ADAPTED CONTROLLER	HARRAND, MICHEL
<a href="#"><u>09730498</u></a>	<a href="#"><u>6631441</u></a>	150	12/04/2000	DRAM READ AND WRITE CIRCUIT	HARRAND, MICHEL
<a href="#"><u>09769009</u></a>	Not Issued	160	01/24/2001	Fast structure dram	HARRAND, MICHEL
<a href="#"><u>09827298</u></a>	Not Issued	160	04/05/2001	Image predictor	HARRAND, MICHEL
<a href="#"><u>09915761</u></a>	<a href="#"><u>6944748</u></a>	150	07/26/2001	Signal processor executing variable size instructions using parallel memory banks that do not include any no-operation type codes, and corresponding method	HARRAND, MICHEL
<a href="#"><u>09927085</u></a>	Not Issued	161	08/09/2001	Fast structure DRAM	HARRAND, MICHEL
<a href="#"><u>10055510</u></a>	Not Issued	160	01/23/2002	Fast structure DRAM	HARRAND, MICHEL
<a href="#"><u>10075001</u></a>	Not Issued	161	02/13/2002	Fast random access DRAM management method	HARRAND, MICHEL
<a href="#"><u>10668060</u></a>	Not Issued	41	09/22/2003	Fast random access DRAM management method	HARRAND, MICHEL
<a href="#"><u>10766291</u></a>	Not Issued	71	01/26/2004	Process for refreshing a dynamic random access memory and corresponding device	HARRAND, MICHEL
<a href="#"><u>11018864</u></a>	Not Issued	30	12/21/2004	Dynamic random access memory having at least two buffer registers and method for controlling such a memory	HARRAND, MICHEL
<a href="#"><u>11301635</u></a>	Not Issued	20	12/13/2005	Memory circuit, such as a DRAM, comprising an error correcting mechanism	HARRAND, MICHEL
<a href="#"><u>11346796</u></a>	Not Issued	30	02/03/2006	Dynamic memory for a cellular terminal	HARRAND, MICHEL

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name

First Name

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | Home page

Day : Friday  
 Date: 5/19/2006  
 Time: 11:59:20


**PALM INTRANET**
**Inventor Name Search Result**

Your Search was:

Last Name = BULONE

First Name = JOSEPH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08662778</a>	6138264	150	06/10/1996	SYNDROME CALCULATION CIRCUIT	BULONE, JOSEPH
<a href="#">10075001</a>	Not Issued	161	02/13/2002	Fast random access DRAM management method	BULONE, JOSEPH
<a href="#">10668060</a>	Not Issued	41	09/22/2003	Fast random access DRAM management method	BULONE, JOSEPH
<a href="#">10766291</a>	Not Issued	71	01/26/2004	Process for refreshing a dynamic random access memory and corresponding device	BULONE, JOSEPH

Inventor Search Completed: No Records to Display.

<b>Search Another: Inventor</b>	<b>Last Name</b>	<b>First Name</b>
	<input type="text" value="bulone"/>	<input type="text" value="joseph"/>
		<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

 Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | Home page